

- ① a. ASIC - Application Specific integrated Circuit
- b. PAL - Programmable array logic
- c. PLA - Programmable logic array
- d. PLD - Programmable logic device
- e. CPLD - Complex programmable logic device.
- f. FPGA - Field programmable gate array

② Typically higher granularity level results in lesser delay between input & output. As the granularity of logic blocks increases, numbers of levels of logic in critical path decreases, and hence delay in critical path decreases.