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MATRIC: 17/SCI01/059

COURSE CODE: CSC 310

COURSE TITLE:  Computer Architecture and Organization II

1. **RISC**, or Reduced Instruction Set Computer. is a type of microprocessor **architecture** that utilizes a small, highly-optimized set of instructions, rather than a more specialized set of instructions often found in other types of **architectures**.

2. CISC. Stands for "**Complex Instruction Set Computing**." This is a type of microprocessor design. The CISC architecture contains a large set of computer instructions that range from very simple to very complex and specialized.

3. Very long instruction word (**VLIW**) describes a computer processing **architecture** in which a language compiler or pre-**processor** breaks program instruction down into basic operations that can be performed by the **processor** in parallel (that is, at the same time).