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* **MATRIC NUMBER: 17/SCI01/009**
* **COURSE CODE: CSC310**
* **COURSE TITLE: Computer Architecture and Organisation II**

**ASSIGNMENT**

* A torus interconnect is a switch-less network topology for connecting processing nodes in a parallel computer system
* **Hypercube networks** are a type of **network** topology used to connect multiple processors with memory modules and accurately route data. **Hypercube networks** consist of 2m nodes. These nodes form the vertices of squares to create an internetwork connection.